## United States Patent [19]

## Wasmer et al.

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[54]	RECTIFIER AND METHOD					
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[51]	Int. Cl.5	•••••••	H01L 23/28; H01L 23/02; H01L 23/12; H01L 23/42			
[52]	U.S. Cl	••••••				
[58]	Field of Se	arch	357/68, 72, 77, 79, 357/76			
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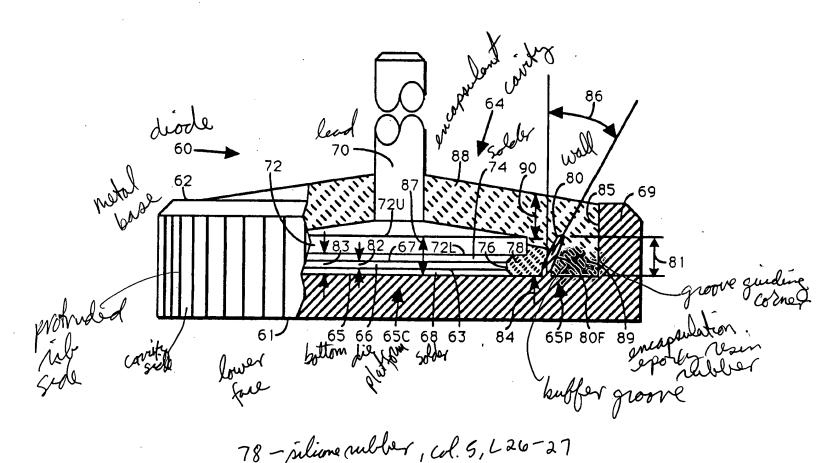
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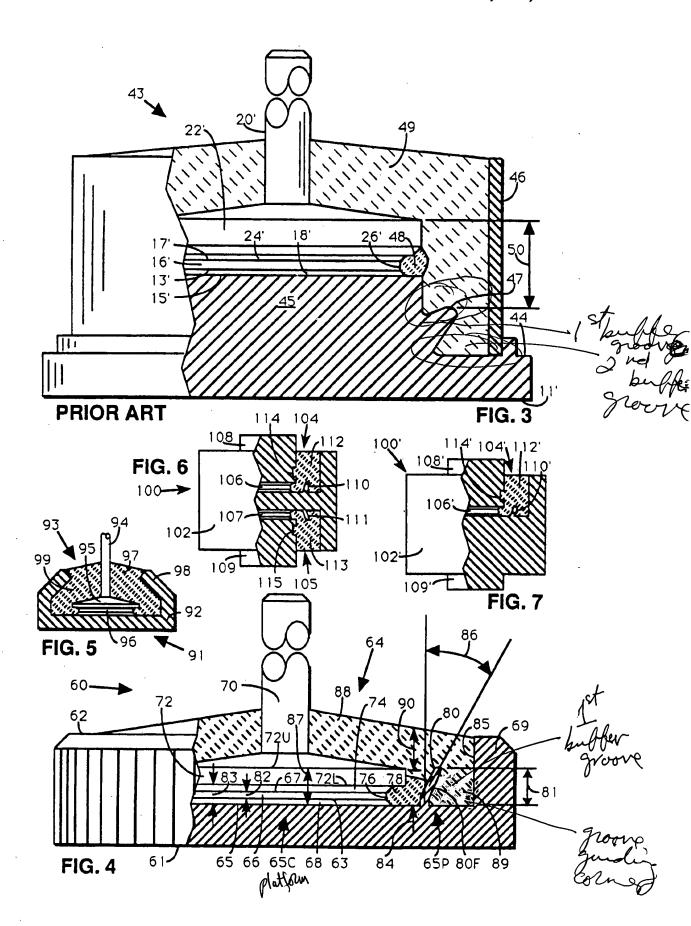
[57] ABSTRACT

A rectifier (60) is formedf by soldering a diode chip (66) in a cavity (64) in a metal base (62) having a metal sidewall (69), soldering the head (72) of an axial lead (70) to the chip (66), and filling the cavity (64) with an encapsulation (88). An outward leaning partition (80) is provided in the cavity (64) around and at about the same elevation as the chip (66). The encapsulation (88) covers the lead head (72) and the partition (80), and fills the space between the partition (80) and the base sidewall (69). This locks all the parts together, giving improved reliability and lead stiffnes at low cost.

10 Claims, 2 Drawing Sheets



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